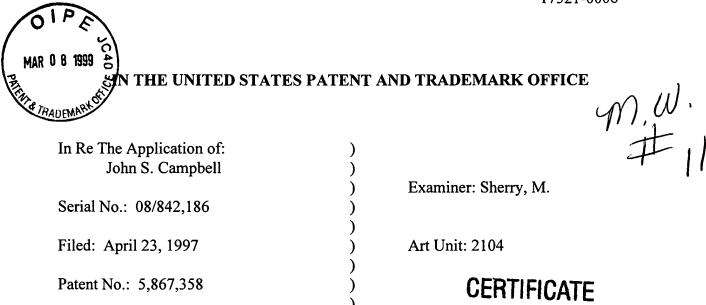
PATENTS 17521-0006



For: FAULT CURRENT LIMITING

CIRCUIT

Patented: February 2, 1999

OF CORRECTION

MAR 1 6 1999

Cesari and McKenna, LLP Apple 30 Rowes Wharf Boston, MA 02110 March 8, 1999

CERTIFICATE OF EXPRESS MAILING

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I hereby certify that the following paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service pursuant to 37 C.F.R. §1.10 in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231 on March 8, 1999.

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/Jonathan Gabriel

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Sir:

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 C.F.R. §1.323

Please correct the following:

At column 7, claim 8, line 30, change "said" to --second--.

We are enclosing a check in the amount of \$100.00 to cover the cost of this request.

Please charge any additional fee occasioned by this paper to our Deposit Account No. 03-1237.

Respectfully submitted,

John F. McKenna

Reg. No. 20,912

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30 Rowes Wharf

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. :

5,867,358

DATED

February 2, 1999

INVENTOR(S):

John S. Campbell

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 7, claim 8, line 30, change "said" to --second--

Į,

John F. McKenna, Esq. CESARI AND MCKENNA, LLP 30 Rowes Wharf Boston, MA 02110